

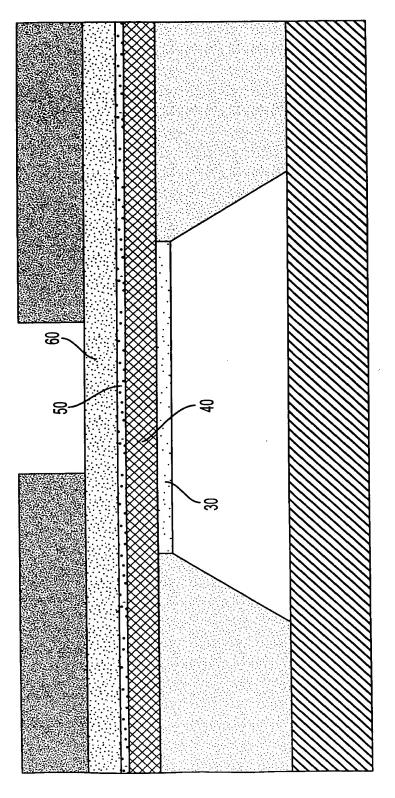
FIG. 1A

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Title: METHOD FOR FORMING A SUPER SELFALIGNED HETERO-JUNCTION BIPOLAR
TRANSISTOR
Inventor(s): Kalnitsky, et al.

60305 2 7201

...ct No.: 186-82.



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Inventor(s): Kainitsky, et al.
Attorney Docket No.: 60305-307201
Application No.: 09/882,538 Sheet 3 of 9
Filling Date: 06/15/2001

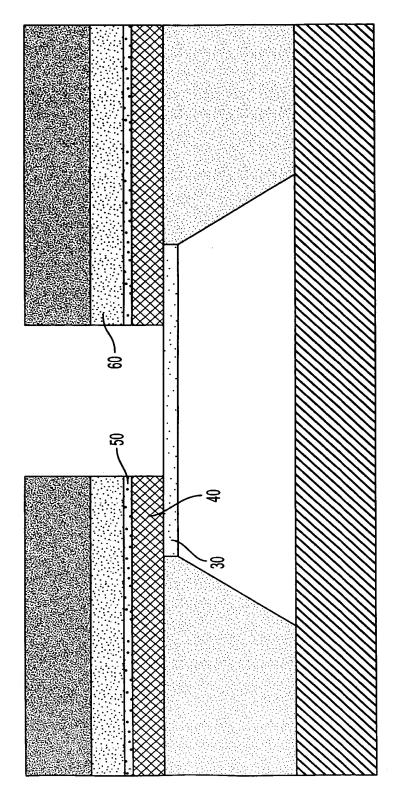
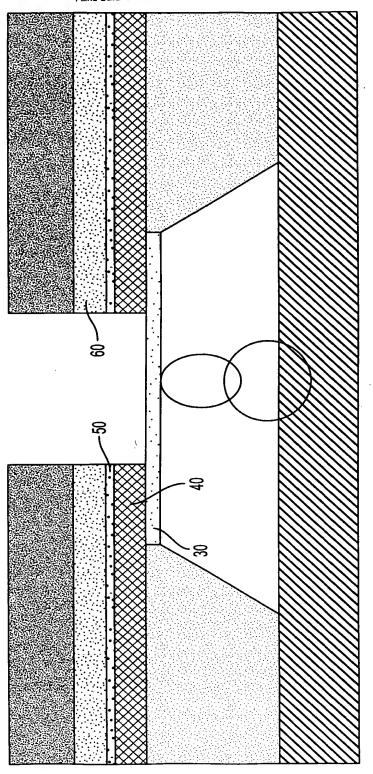
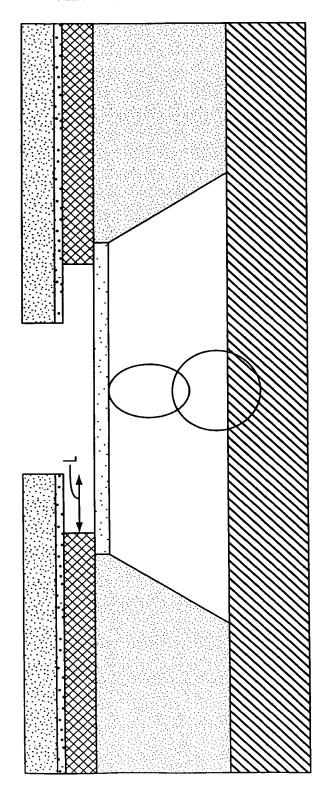


FIG. 1C

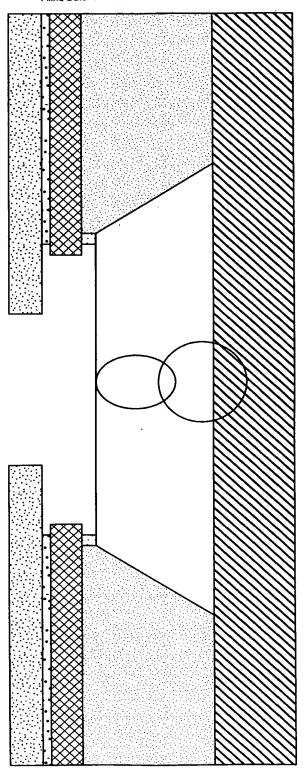
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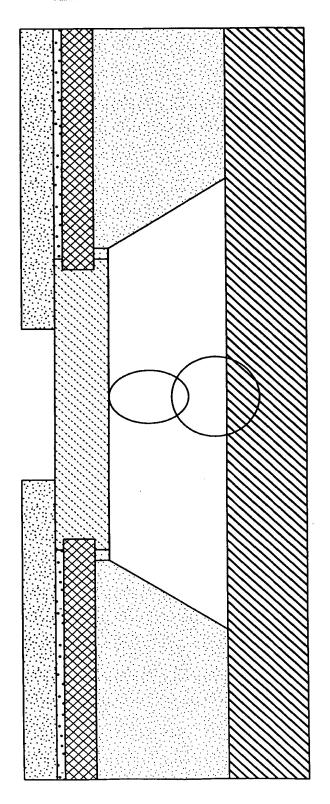
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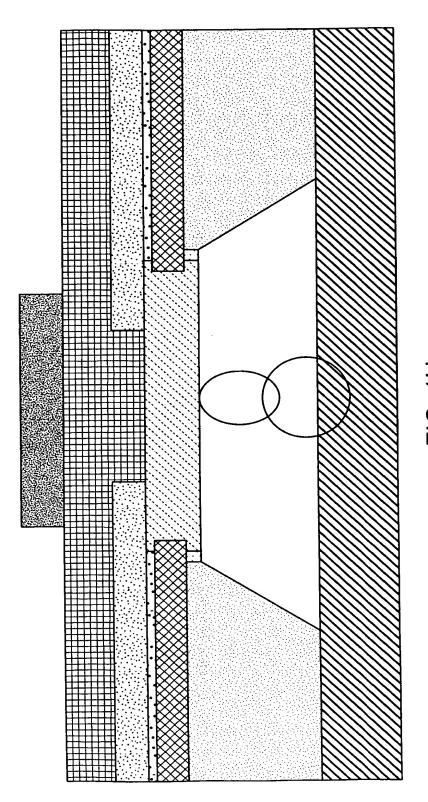


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